**FIG. 1**

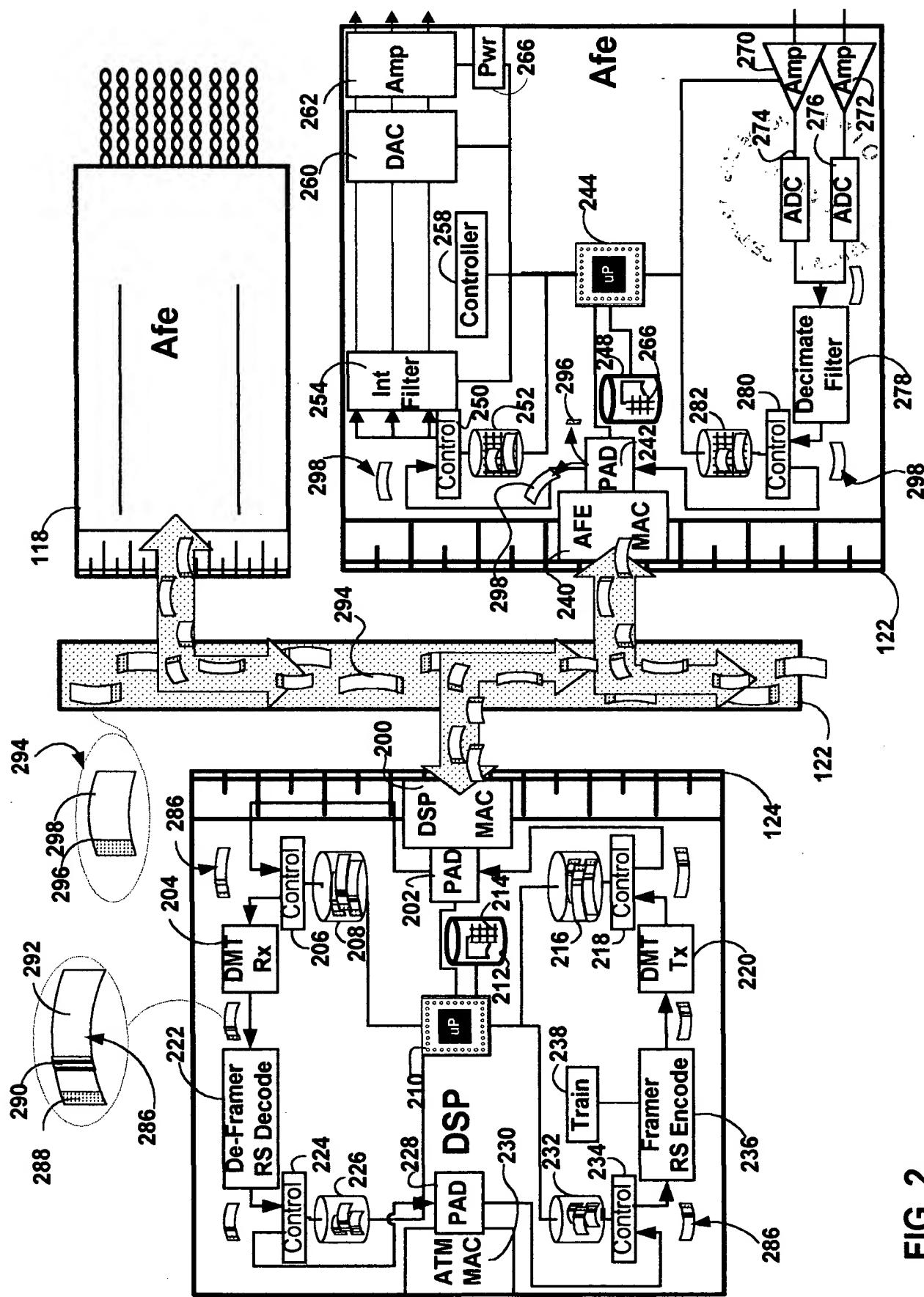


FIG. 2

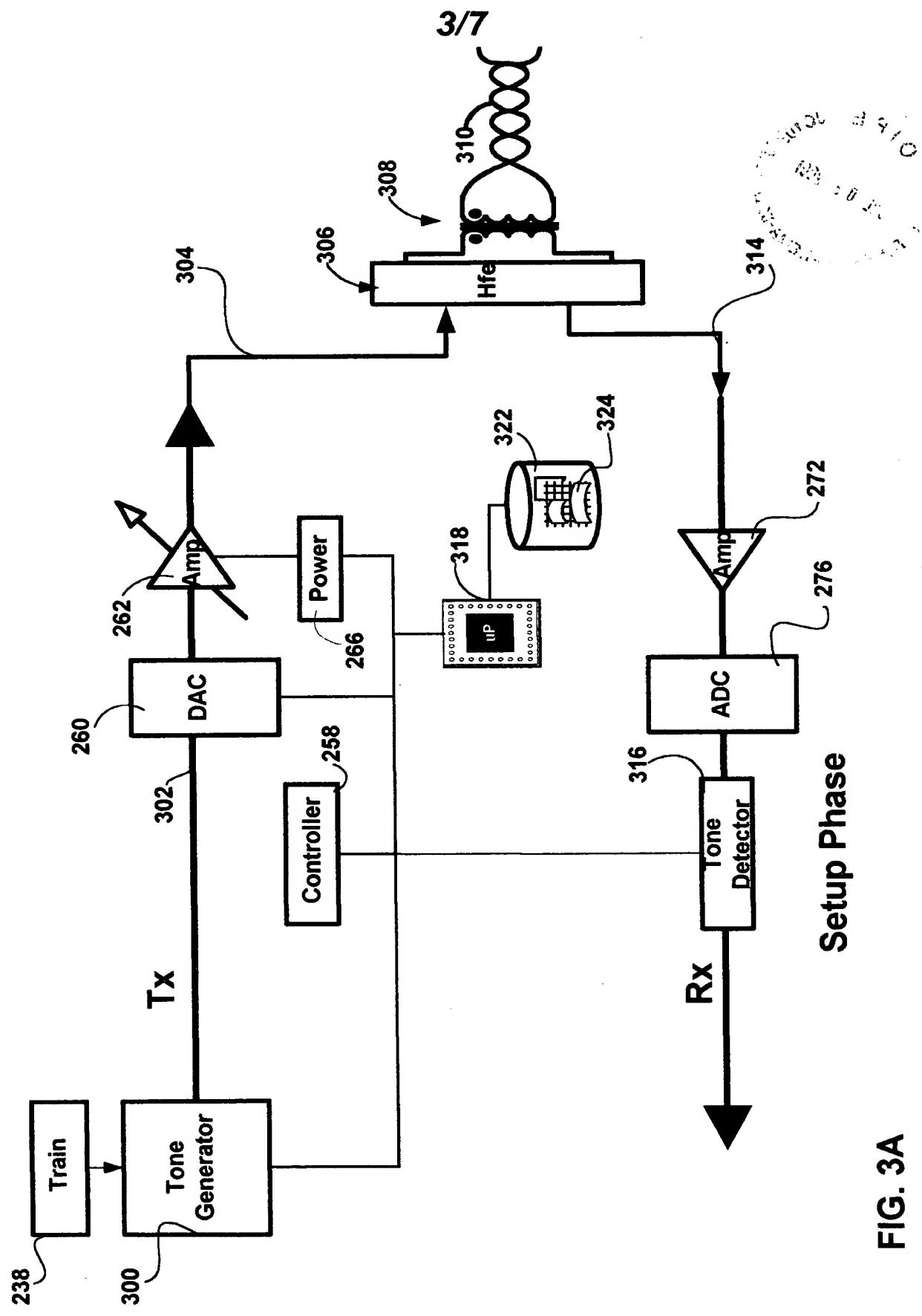


FIG. 3A

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01000000000000000000000000000000

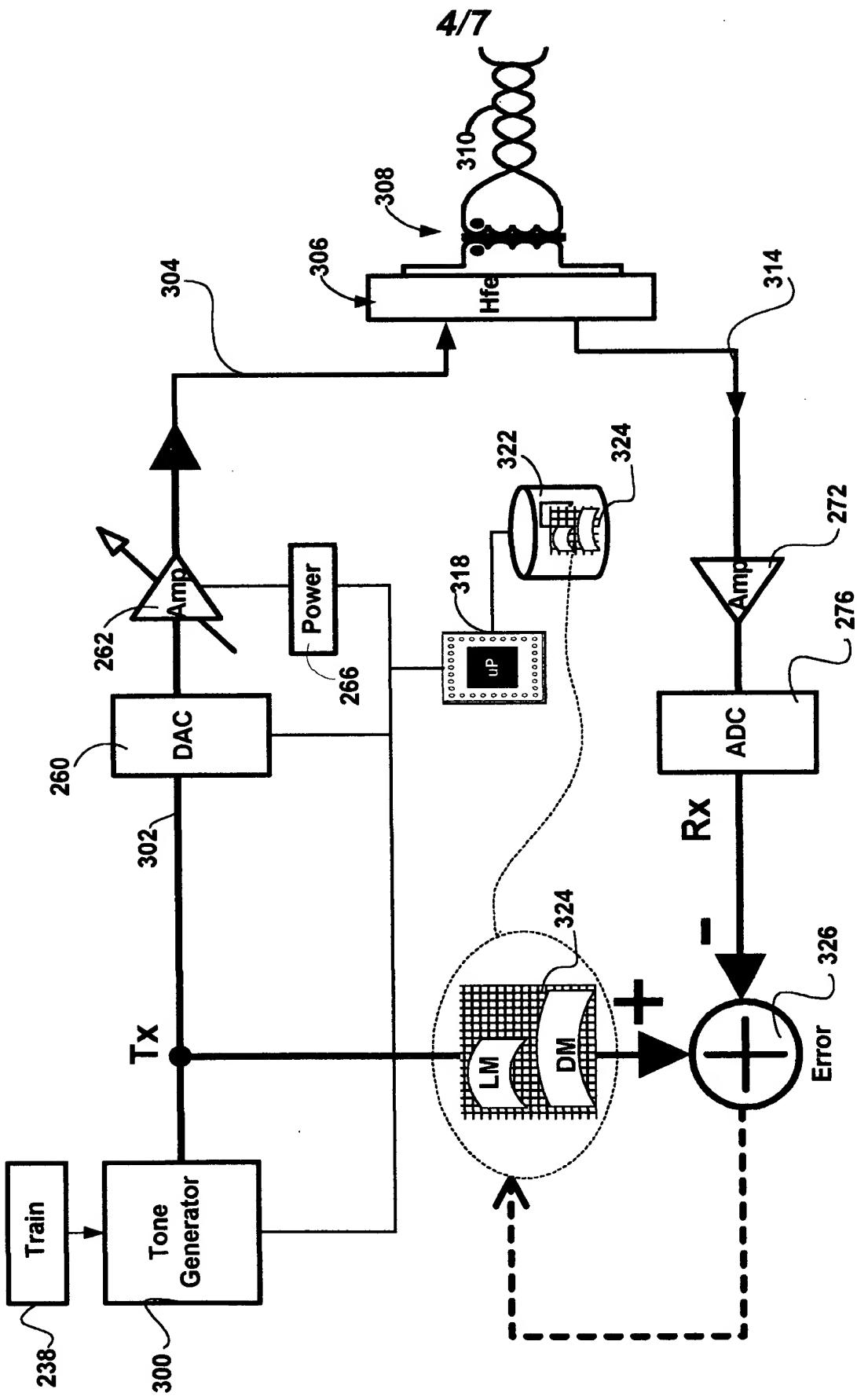


FIG. 3B

Configuration Phase

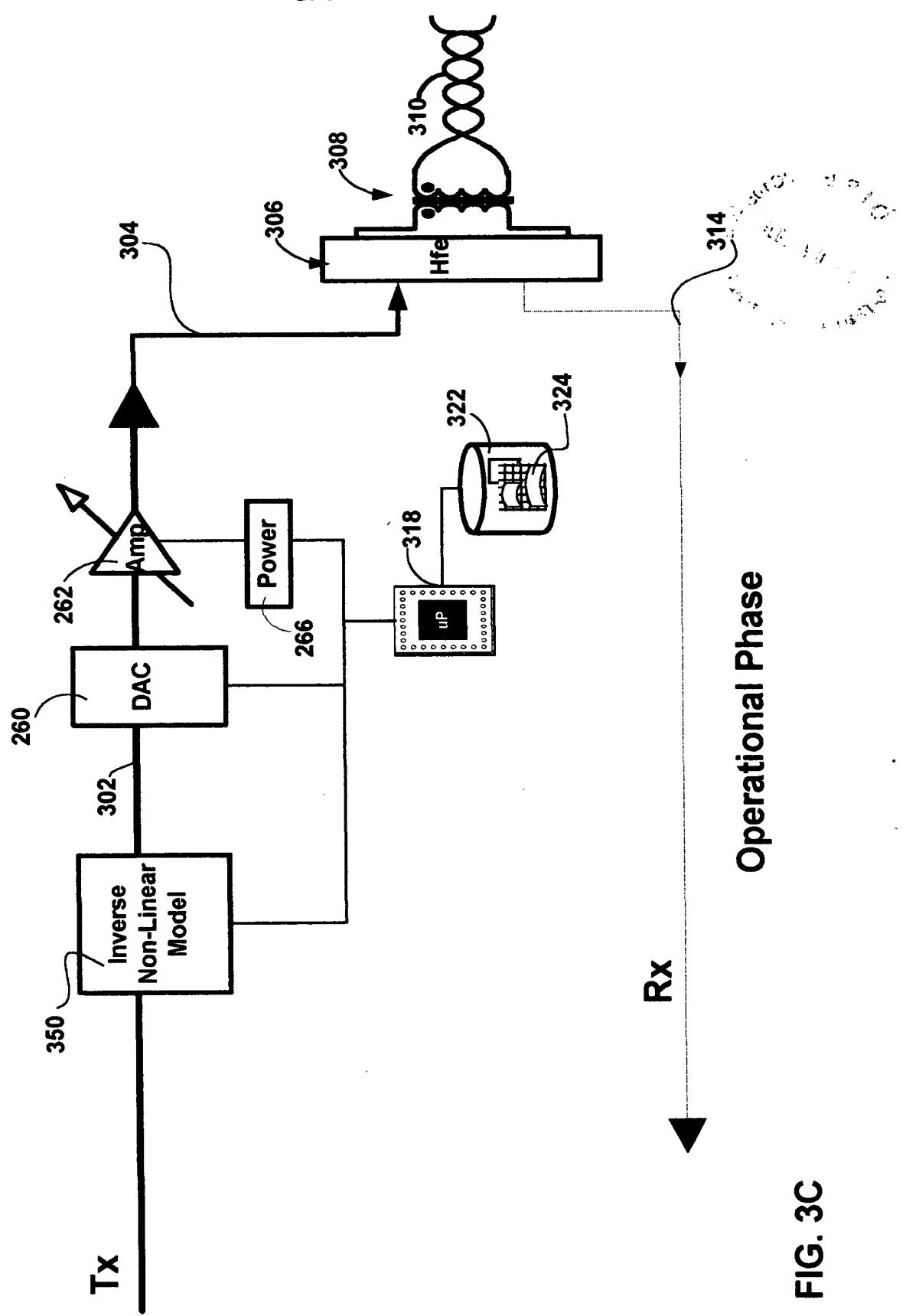


FIG. 3C

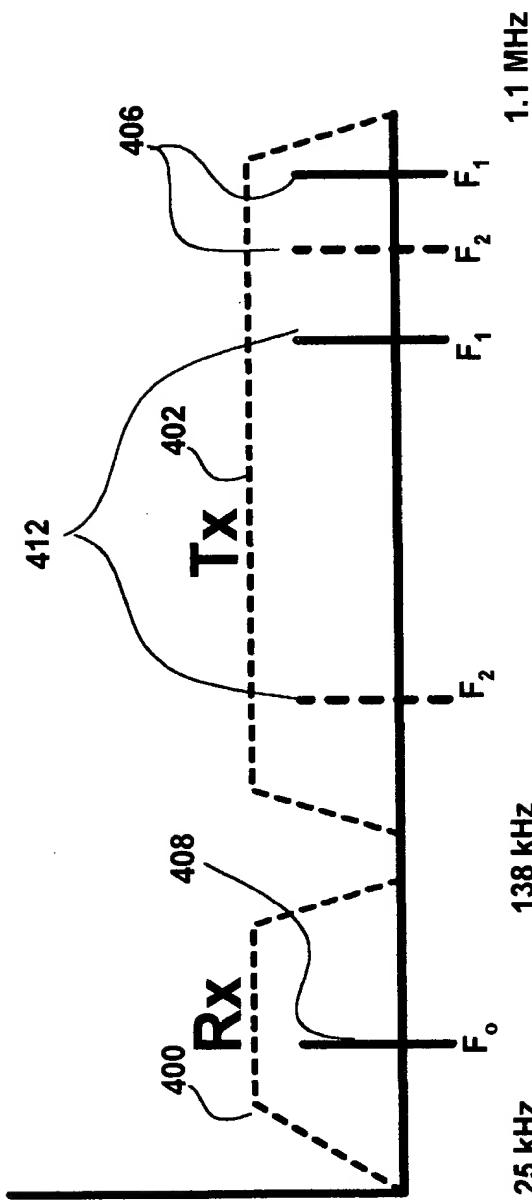


FIG. 4A

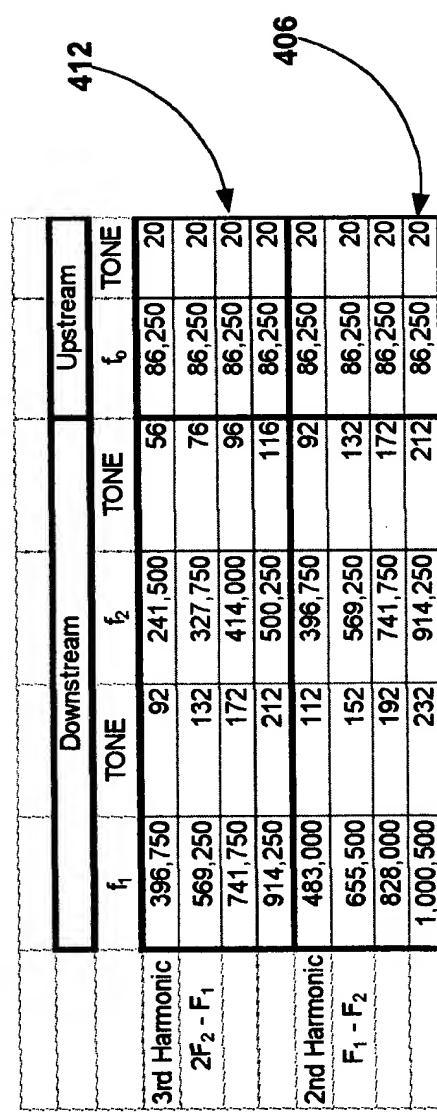
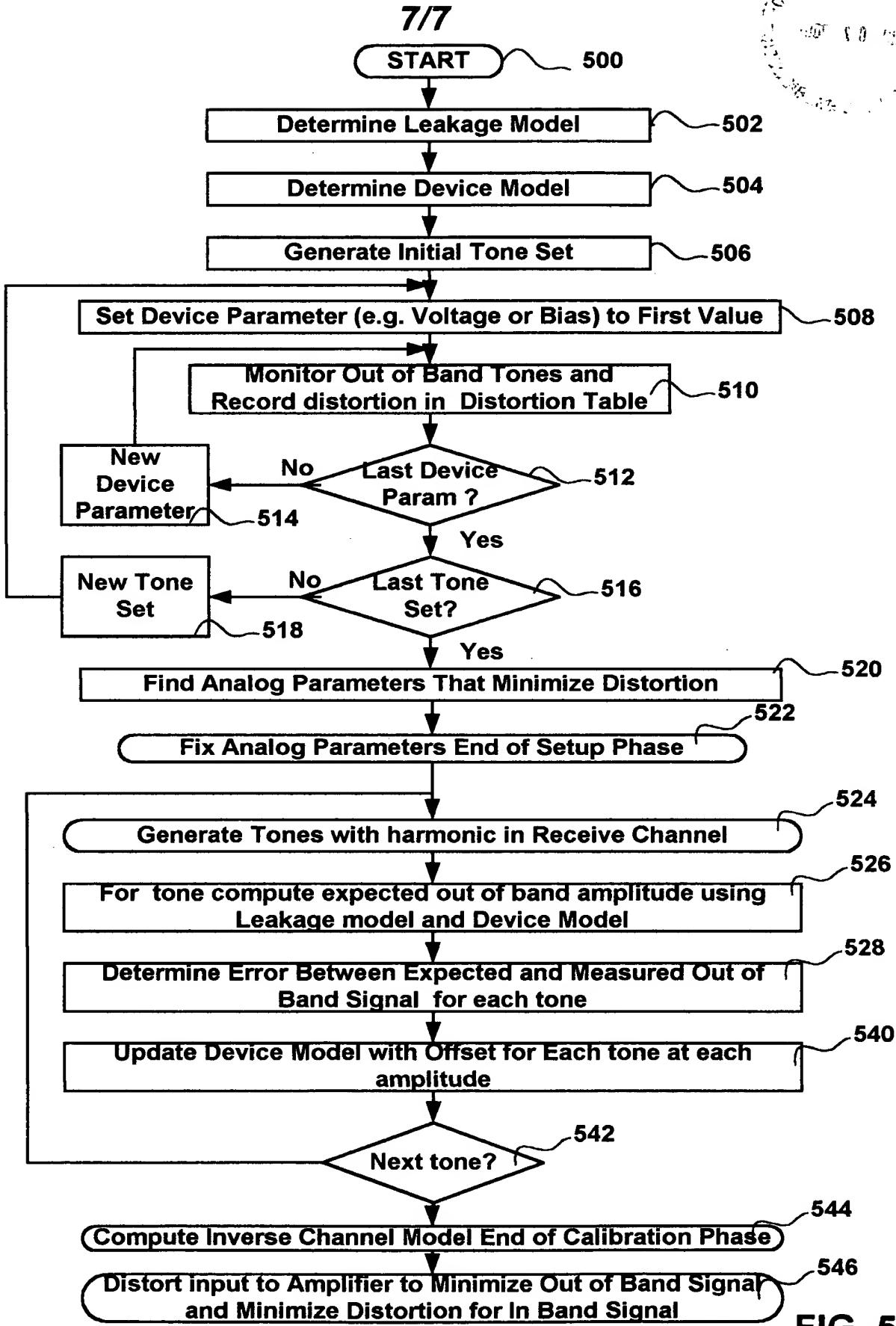


FIG. 4B



**FIG. 5**